

AMENDMENTS TO THE SPECIFICATION

Please amend the specification as indicated below:

[0055] The clock data storage section 31 includes a register to store clock data, for example, showing the frequency of an external clock signal. In this example, clock data, e.g., a code 1 or a code 0 can be stored to show the clock signal frequencies of 400 MHz and 300 MHz. The clock data is one bit. The number of kinds of the external clock signal may be equal to or more than three and the clock data storage section 31 stores the clock data to distinguish them. The clock data is supplied from an external unit through an external input terminal 32 at any time and stored in the register. Also, the clock data ~~does not have the register, and~~ may be previously set ~~previously in the process of~~ during manufacturing, for example, by using a fuse circuit and a bonding option. The clock data, still referring to FIG. 6, is outputted as cyc300 or cyc400, which corresponds to the external clock signal of 300 MHz or 400 MHz, respectively.